

ABSTRACT OF THE DISCLOSURE

A field emission display includes first and second substrates provided with a predetermined gap therebetween. At least one gate electrode is formed on the first substrate. Cathode electrodes are formed in a predetermined pattern, the cathode electrodes including openings where the cathode electrodes intersect the gate electrode. An insulation layer is formed between the at least one gate electrode and the cathode electrodes. Counter electrodes are formed smaller than and within the openings. Emitters are provided contacting the cathode electrodes. An anode electrode is formed on at least one side of the second substrate. Phosphor layers are formed in a predetermined pattern on at least one side of the anode electrode. Two of the emitters are provided on opposite sides of each of the counter electrodes, each of the emitters including two short sides contacting the corresponding cathode electrode and one long side.